code words) of a full set of code words. The selected set of code words is selected such that each stream of encoded data (comprising only such code words) transmitted over a serial link has a bit pattern that is less susceptible to inter-symbol interference ("ISI") during transmission than is the bit pattern determined by a conventionally encoded version of the same data (comprising not only the selected set of code words but also other members of the full set). In general, the best choice for the selected set of code words selected from a full set of binary code words depends on the particular coding implemented by the full set. Typically, the selected set of code words includes words whose serial patterns (during transmission) have fewer contiguous zeros and ones (and thus are less susceptible to ISI during transmission) than do those code words in the full set that are not selected. In preferred embodiments in which the bits of the selected set of code words are transmitted over a serial link as sequences of rising and falling voltage transitions, the bit pattern of each transmitted stream of the selected set of code words implements DC balancing to limit the voltage drift over time.

IN THE SPECIFICATION:

[attached is an Appendix including a marked up version of the amended sections of the specification showing the differences between the text as originally filed, and the text as hereby amended]:

Replace page 2, line 14, with the following text: --over conductor pairs);--

Replace page 23, line 22, with the following text: --the bottom of Fig. 6) is transmitted--.

Replace page 27, line 4, with the following text: -- available the appropriate number of guard band words is a factor in the selection of the--.

Replace page 29, line 9, with the following text: --bits). Each possible value of the M-bit source data has a preselected code in the 2^M --.

Replace page 29, line 11, with the following text: --are mapped to inventive code words in the 2^M word space which are then transmitted--.

Replace page 29, line 31, with the following text: --and Q[8]) present greater error risks than do the other bits. Any bit error occurring--.